



<b>Form 1449 (Modified)</b>  <b>Information Disclosure Statement By Applicant</b>  (Use Several Sheets if Necessary)	Atty Docket No. <b>02-6037/LSI1P218</b> Applicant: <b>ARONOWITZ et al.</b> Filing Date <b>October 31, 2003</b>	Application No.: <b>10/698,169</b>  Group <b>2814</b>
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#### U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
<i>A.M.</i>	A	2001/0038135A	11/08/01	Forbes et al.			05/15/01
	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						

#### Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	J							
	K							
	L							
	M							
	N							

#### Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
<i>A.M.</i>	O	Stanley Wolf et al., "Silicon Processing for the VLSI Era, Vol. 1: Process Technology", Lattice Press, Sunset Beach, CA, 1986, pg. 303-306.
	P	
	Q	
Examiner	<i>ANN D. MAJ</i>	
	Date Considered	<i>6/22/05</i>

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.